# KBJ4005 THRU KBJ410

# Yixin

### GLASS PASSIVATED SINGLE-PHASE BRIDGE RECTIFIER

REVERSE VOLTAGE: 50 to 1000 VOLTS FORWARD CURRENT: 4.0 AMPERE

#### **FEATURES**

· Glass passivated chip junction

· Reliable low cost construction utilizing molded plastic technique

- · Ideal for printed circuit board
- · Low forward voltage drop
- · Low reverse leakage current
- · High surge current capability

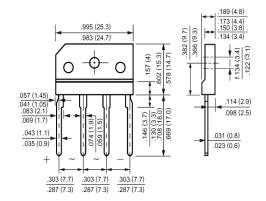
#### **MECHANICAL DATA**

Case: Molded plastic, KBJ

Epoxy: UL 94V-O rate flame retardant

Terminals: Leads solderable per MIL-STD-202,

method 208 guaranteed Mounting position: Any Weight: 0.16ounce, 4.6gram KBJ



**Dimensions in inches and (millimeters)** 

## Maximum Ratings and Electrical Characteristics

Single phase, half wave, 60Hz, resistive or inductive load.

For capacitive load, derate current by 20%.

	Symbols	KBJ4005	KBJ401	KBJ402	KBJ404	KBJ406	KBJ408	KBJ410	Units
Maximum Recurrent Peak Reverse Voltage	V <sub>RRM</sub>	50	100	200	400	600	800	1000	Volts
Maximum RMS Voltage	V <sub>RMS</sub>	35	70	140	280	420	560	700	Volts
Maximum DC Blocking Voltage	V <sub>DC</sub>	50	100	200	400	600	800	1000	Volts
Maximum Average Forward	т.		40						
Rectified Current at T <sub>C</sub> =115℃	I <sub>(AV)</sub>	4.0							Amp
Peak Forward Surge Current,									
8.3ms single half-sine-wave	I <sub>FSM</sub> 120								Amp
superimposed on rated load (JEDEC method)									
Maximum Forward Voltage	V	1.0							Volts
at 2.0A DC and 25℃	$\mathbf{V_F}$								
Maximum Reverse Current at T <sub>A</sub> =25℃	т	5.0							
at Rated DC Blocking Voltage T <sub>A</sub> =125℃	$I_R$		500						uAmp
Typical Junction Capacitance (Note 1)	C <sub>J</sub>	40							pF
Typical Thermal Resistance (Note 2)	$R_{\theta JC}$	5.5							°C/W
Operating and Storage Temperature Range	T <sub>J</sub> , Tstg	-55 to +150							ဗ

#### NOTES:

- 1- Measured at 1 MHz and applied reverse voltage of 4.0 VDC.
- 2- Thermal Resistance from Junction to Case with Device Mounted on 75mm x 75mm x 1.6mmCu Plate Heatsink.



#### RATINGS AND CHARACTERISTIC CURVES

